



Appl. No. 10/779,416  
Amdt. dated November 12, 2004  
Reply to Office Action of August 13, 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): B. Arlen Young

Assignee: Adaptec, Inc.

Title: A METHOD FOR CONFIGURING A SINGLE HARDWARE I/O  
CONTROL BLOCK ARCHITECTURE FOR USE WITH BOTH  
MIRRORED AND NON-MIRRORED DATA TRANSFERS (AS  
AMENDED)

Serial No.: 10/779,416 Filed: February 12, 2004

Examiner: Tanh Q. Nguyen Group Art Unit: 2182

Docket No.: ADPT105103

Monterey, CA  
November 12, 2004

Mail Stop Amendment  
Honorable Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

AMENDMENT

Dear Sir:

In response to the Office Action dated **August 13, 2004**,  
please amend the above-identified application as follows:

1. **Amendments to the Specification** begin on page 2  
of this paper;
2. **Amendments to the Claims** are reflected in the  
listing of Claims which begins on page 3 of this paper;  
and
3. **Remarks** begin on page 6 of this paper.